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|----------------------------------|---------------------------|--|---------------------------|
| December 22, 2003<br>Date Mailed | Adrian Villarreal<br>Name | <br>Signature | December 22, 2003<br>Date |
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## Patent

In Re Patent Application of:

Examiner: Not Yet Assigned

Art Unit: Not Yet Assigned

For: HYBRID OPTICAL BURST SWITCHING  
WITH FIXED TIME SLOT ARCHITECTURE

## INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):


- X   37 C.F.R. §1.97(b).
- 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
- A certification pursuant to 37 C.F.R. §1.97(e) or
- A check for \$\_\_\_\_\_ for the fee under 37 C.F.R. § 1.17(p).
- 37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:
- (1) A certification pursuant to 37 C.F.R. §1.97(e);
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  - (3) A check for \$\_\_\_\_\_ for the fee under 37 C.F.R. §1.17(i) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

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Dated: Dec. 22, 2003

  
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| <b>Substitute for Form 1449/PTO</b><br><br><b>INFORMATION DISCLOSURE</b><br><br><b>STATEMENT BY APPLICANT</b><br><i>(use as many sheets as necessary)</i> |   |    |   | <b>Complete if Known</b> |                        |
|   |   |    |   | Application Number       | New Patent Application |
|   |   |    |   | Filing Date              | Herewith               |
|   |   |    |   | First Named Inventor:    | Mishra et al.          |
|   |   |    |   | Art Unit                 | Not yet assigned       |
|   |   |    |   | Examiner Name            | Not yet assigned       |
| Sheet   | 1 | of | 1 | Attorney Docket Number   | 42P16846               |

| NON PATENT LITERATURE DOCUMENTS |                      |  |                |
|---------------------------------|----------------------|--|----------------|
| Examiner Initials*              | Cite No <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T <sup>2</sup> |
|                                 | 1.                   | MYERS, A. et al., "Performance of the Just-Enough-Time (JET) Scheme for Optical Burst Switching", Optical Networks Group, Dept. Of Electronic & Electrical Engineering, UCL (University College London).   |                |
|                                 | 2.                   | BALDINE, Ilia et al., "Jumpstart: a Just-in-Time Signaling Architecture for WDM Burst-Switched Networks", MCNC ANR, Research Triangle Park, NCSU Department of Computer Science, NC, USA.  |                |
|                                 | 3.                   | OVADIA, Shlomo et al., "Photonic Burst Switching (PBS) Architecture for Hop and Span-Constrained Optical Networks", Intel Corporation, Department of Electrical & Computer Engineering, University of California Santa Barbara, CA 93106.                      |                |
|                                 | 4.                   | DOLZER, Klaus et al., "Evaluation of Reservation Mechanisms for Optical Burst Switching", September 18-19, 2000, Institute of Communication Networks and Computer Engineering, University of Stuttgart.  |                |
|                                 | 5.                   | BALDINE, Ilia et al., "Just-in-Time Optical Burst Switching Implementation in the ATDnet All-Optical Networking Testbed", MCNC Research and Development Institute, Advanced Networking Research Division, NC, USA.   |                |
|                                 | 6.                   | WEI, John Y., "Just-in-Time Switching and NGI SuperNet Network Control Research", October 26, 1998, Bellcore.  |                |
|                                 | 7.                   | BANERJEE, Ayan et al., "Generalized Multiprotocol Label Switching: An Overview of Signaling Enhancements and Recovery Techniques", Topics of Internet Technology, July 2001, pages 144-151, IEEE Communications Magazine.                                      |                |
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|--------------------|-----------------|
| Examiner Signature | Date Considered |
|--------------------|-----------------|

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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